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APPLICANT
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		11-213673	08/06/1999	MATSUSHITA ELECTRIC IND CO LTD		Japanese (w/ English Abstract)	1
		09-73784	03/18/1997	NEC CORP		Japanese (w/ English Abstract)	
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	A	P2003-60089	02/28/2003	MITSUBISHI ELECTRIC CORP		Japanese (w/ English Abstract)	
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		Hiroshi KAWAGUCHI et al., "DYNAMIC LEAKAGE CUT-OFF SCHEME FOR LOW-VOLTAGE SRAM's", IEEE 1998 Symposium on VLSI Circuits Digest of Technical Papers, pp. 140-141

EXAMINER

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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